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[54] MAIL PROCESSING SYSTEM CONTROLLER

[75] Inventors: **Peter C. DiGiulio**, Fairfield; **Michael L. Hernandez**, Hamden; **Frank D. Ramirez**, Stamford; **Mark W. Van Gorp**, Danbury, all of Conn.

[73] Assignee: **Pitney Bowes Inc.**, Stamford, Conn.

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Related U.S. Application Data

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[52] U.S. Cl. **364/134; 364/131; 364/138; 364/228.1; 364/DIG.1**

[58] Field of Search **364/131-134, 364/135, 136, 138, 140, 474.11, DIG. 1, DIG. 2**

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Primary Examiner—Jerry Smith

Assistant Examiner—Thomas E. Brown

Attorney, Agent, or Firm—Charles G. Parks, Jr.; Melvin J. Scolnick

[57] ABSTRACT

The processing system controller is utilized in a mail processing system. The processing system controller provides process control over a plurality of processing subsystems which operate on a mailpiece traversing the processing system. The processing subsystems employing a plurality of prime movers controlled by the processing controller. The processing system controller includes a motor controller in bus communication with the prime movers for control of the prime movers. Sensors monitor the operating conditions and parameter of each subsystems and are in bus communication with a sensor controller. A random access memory (RAM) is in bus communication with the motor controller and the sensor controller. The motor controller and the sensor controller are programmed such that the motor controller can read from the RAM and the sensor controller can write to the RAM. The motor controller reads from the RAM at discrete time intervals and the sensor controller writes to the RAM at other discrete time intervals.

5 Claims, 2 Drawing Sheets

