

United States Patent [19]

DiGiulio et al.

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[54] POSTAGE METER WITH A NON-VOLATILE MEMORY SECURITY CIRCUIT

[76] Inventors: Peter C. DiGiulio, 628 N. Park Ave., Easton, Conn. 06612; Warren G. Hafner, Terry Hill Rd., Lake Carmel, N.Y. 10512; Henry Stalzer, 237 Middle River Rd., Danbury, Conn. 06810

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[58] Field of Search 371/67, 62, 67.1; 364/464.02; 235/380, 382, 382.5, 492

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Primary Examiner—Parshotam S. Lall

Assistant Examiner—Edward R. Cosimano

ABSTRACT

An electronic postage meter with a non-volatile memory security circuit apparatus is disclosed. The security circuit comprises means for limiting the amount of time the memories may be continuously enabled, means for preventing simultaneous enabling of both memories and means for preventing the write enabling of a memory if the write enable signal is active before a memory select signal is active. The circuit prevents memory access when a conflict is sensed across in an output related to the non-volatile memories. The security circuit provides additional protection to the non-volatile memory so that valuable critical accounting information located therein cannot be modified or destroyed.

2 Claims, 7 Drawing Sheets

