

[54] **ELECTRONIC POSTAGE METER HAVING A NONVOLATILE MEMORY SELECTION MEANS**

4,578,774 3/1986 Muller 364/466

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[57] **ABSTRACT**

[21] **Appl. No.:** 710,871

An electronic postage meter having an improved memory selection circuit is disclosed. A custom memory map decoder circuit with resolution down to a single byte location is used to provide selection enabling signals to insure the selection of an appropriate device only when the addresses appropriate to that device are communicated. In accordance with the invention, at least two nonvolatile memories are provided. Writing to either of these nonvolatile memories is inhibited unless one and only one memory is selected. The circuit also prevents the selection of either of the nonvolatile memories in the event that the write strobe signal to the memories is held active.

[22] **Filed:** Mar. 12, 1985

[51] **Int. Cl.⁴** G06F 13/00; G06F 13/12; G06F 7/04

[52] **U.S. Cl.** 364/464; 364/466; 364/900

[58] **Field of Search** ... 364/200 MS File, 900 MS File, 364/464, 466, 567

[56] **References Cited**

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4 Claims, 16 Drawing Figures

